

Re Box No. V

1 Reference is made to the following documents:

D1 : WO 03/030247 A (SIEMENS AKTIENGESELLSCHAFT; HAESE, KERSTIN; AMIGUES, LAURENCE; SCHWARZ) 10 April 2003 (2003-04-10)  
D2 : GB 1 403 786 A (PHILIPS ELECTRONIC ASSOCIATED INDUSTRIES LTD) 28 August 1975 (1975-08-28)  
D3 : US 3 846 825 A (BUDDE H, NL) 5 November 1974 (1974-11-05)

2 Document D1 is regarded as the nearest prior art. It discloses (the references in parentheses relate to this document):  
an arrangement of an electrical component (2) on a substrate (1), wherein an insulating film (3) is present. The insulating film is joined to the substrate in such a way that a surface contour formed by the component (2) is reproduced in a surface contour of the section of the insulating film,  
from which the subject matter of the independent claim 1 differs in that:  
the section of the insulating film having the surface contour has a dielectric strength of at least 10 kV/mm.

2.1 The subject matter of claim 1 is therefore novel (Article 33(2) PCT).  
The object to be achieved by the present invention can therefore be seen in the fact that  
the arrangement is suitable for power components.

2.2 The solution to achieve this object as proposed in claim 1 of the present invention is based on an inventive step (Article 33(3) PCT) for the following reasons:  
By forming a section of the insulating film having the surface contour so that a dielectric strength of at least 10 kV/mm is obtained.

2.3 Claims 2-13 are dependent on claim 1 and therefore also meet the requirements of the PCT with regard to novelty and inventive step.

3 Document D1 is regarded as the nearest prior art. It discloses (the references in parentheses relate to this document):  
a method for producing an arrangement according to one of the preceding points 2.

5.1 The subject matter of claim 14 is therefore novel (Article 33 (2) PCT) and inventive (Article 33 (3) PCT).

5.2 Claims 15-17 are dependent on claim 14 and therefore also meet the requirements of the PCT with regard to novelty and inventive step.